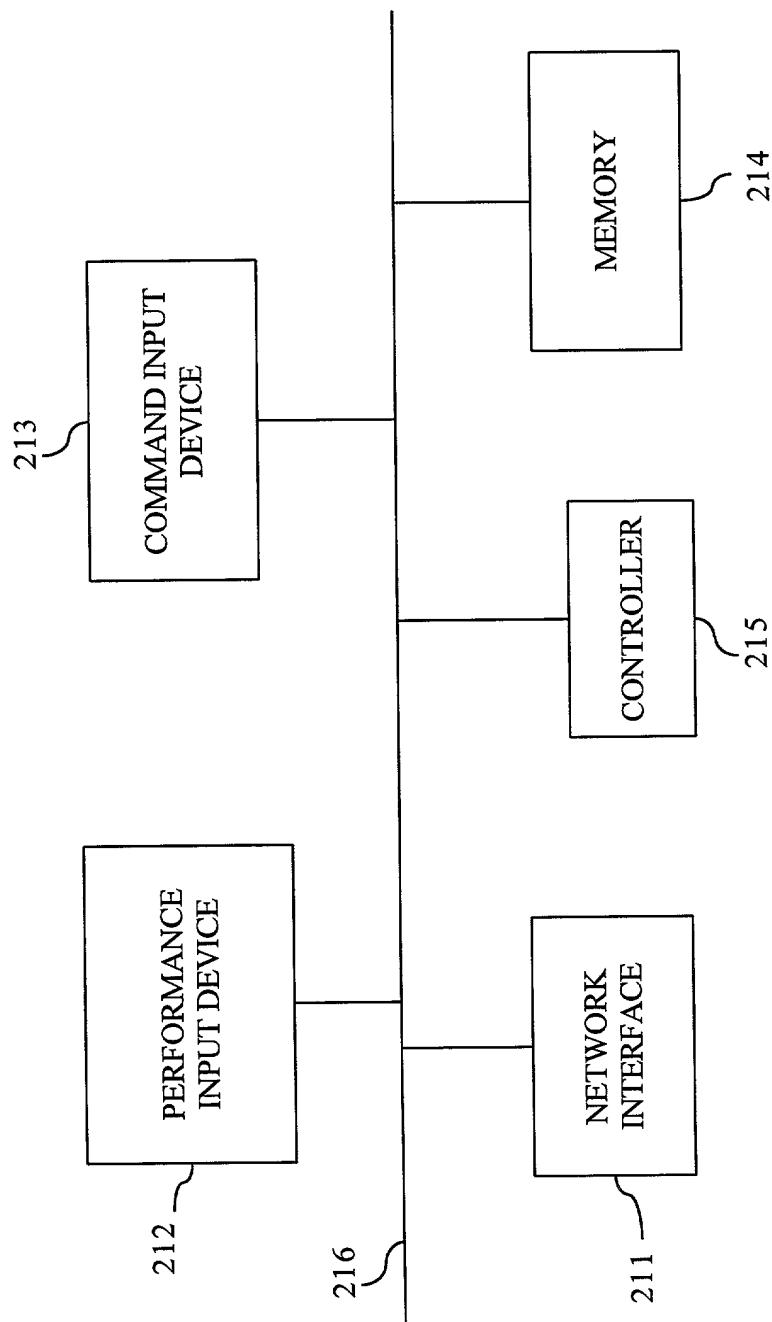


FIG. 1

FIG. 2 is a block diagram of a system 210 according to one embodiment of the present invention. The system 210 includes a performance input device 212, a command input device 213, a network interface 211, a controller 215, and memory 214. The performance input device 212, command input device 213, network interface 211, controller 215, and memory 214 are connected to a common bus 216.



↗
210

FIG. 2

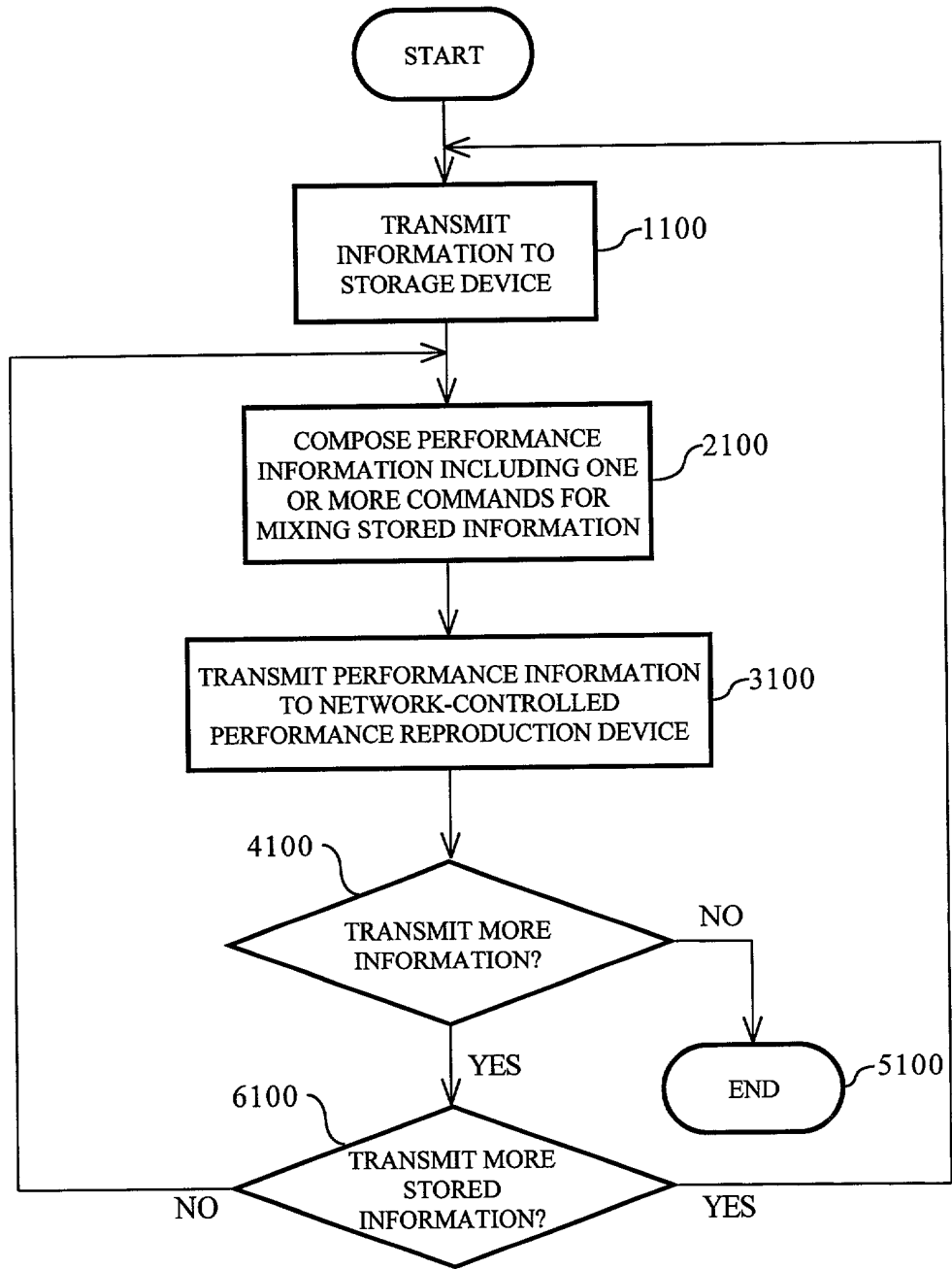


FIG. 3

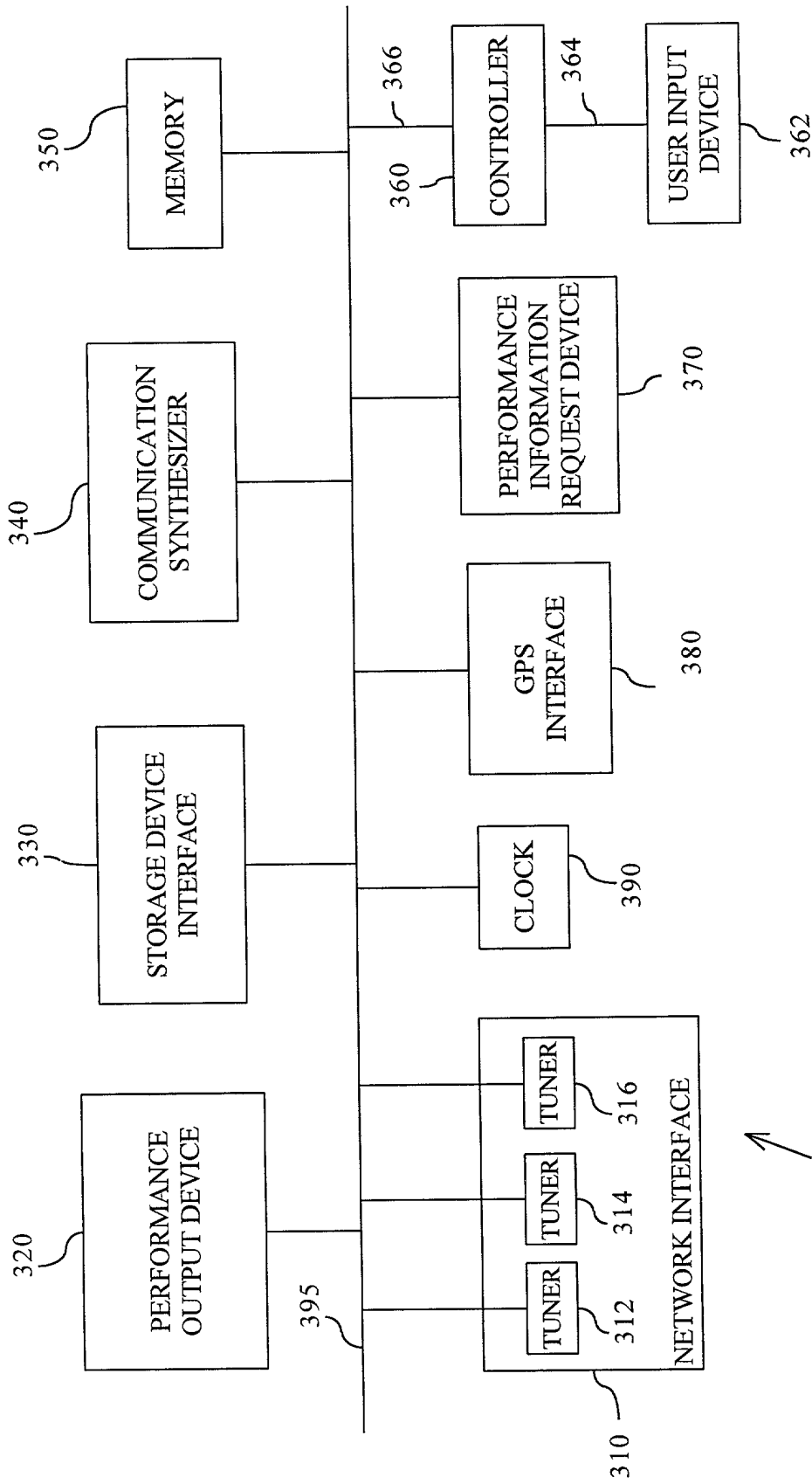


FIG. 4

FIG. 5 is a block diagram of a performance information request device 370. The device 370 includes a performance reproduction device status detector 372, a performance transmitter status detector 371, a request signal generator 375, a storage device status detector 373, and a profile memory 374. The profile memory 374 includes end-user profiles 3742 and performance transmitter profiles 3744. The device 370 is configured to receive a request signal from the request signal generator 375 and to output a performance reproduction device status signal to the performance reproduction device status detector 372. The device 370 is also configured to output a performance transmitter status signal to the performance transmitter status detector 371. The device 370 is further configured to output a storage device status signal to the storage device status detector 373. The device 370 is also configured to output a profile memory signal to the profile memory 374. The profile memory 374 is configured to output end-user profiles 3742 and performance transmitter profiles 3744 to the request signal generator 375.

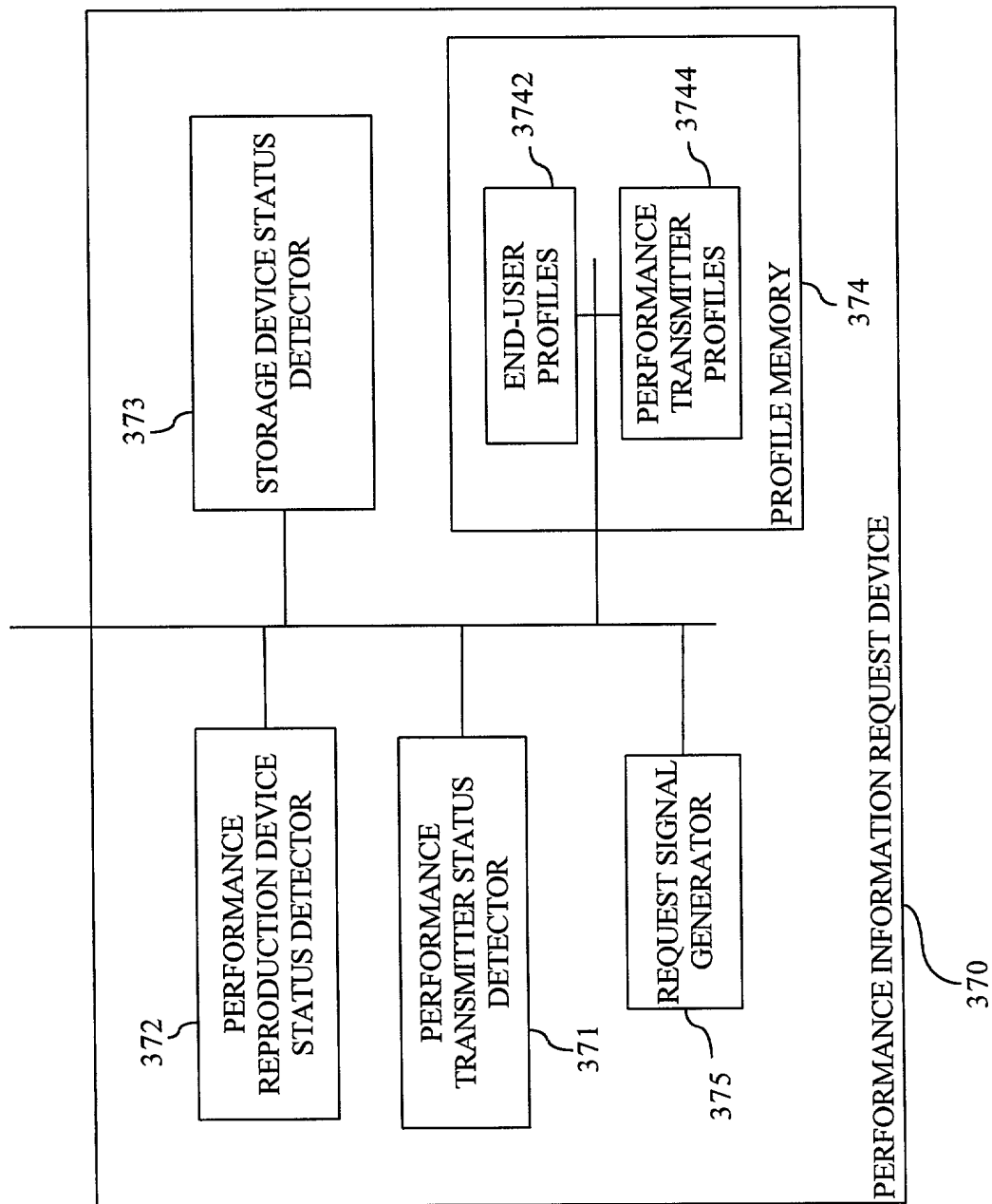


FIG. 5

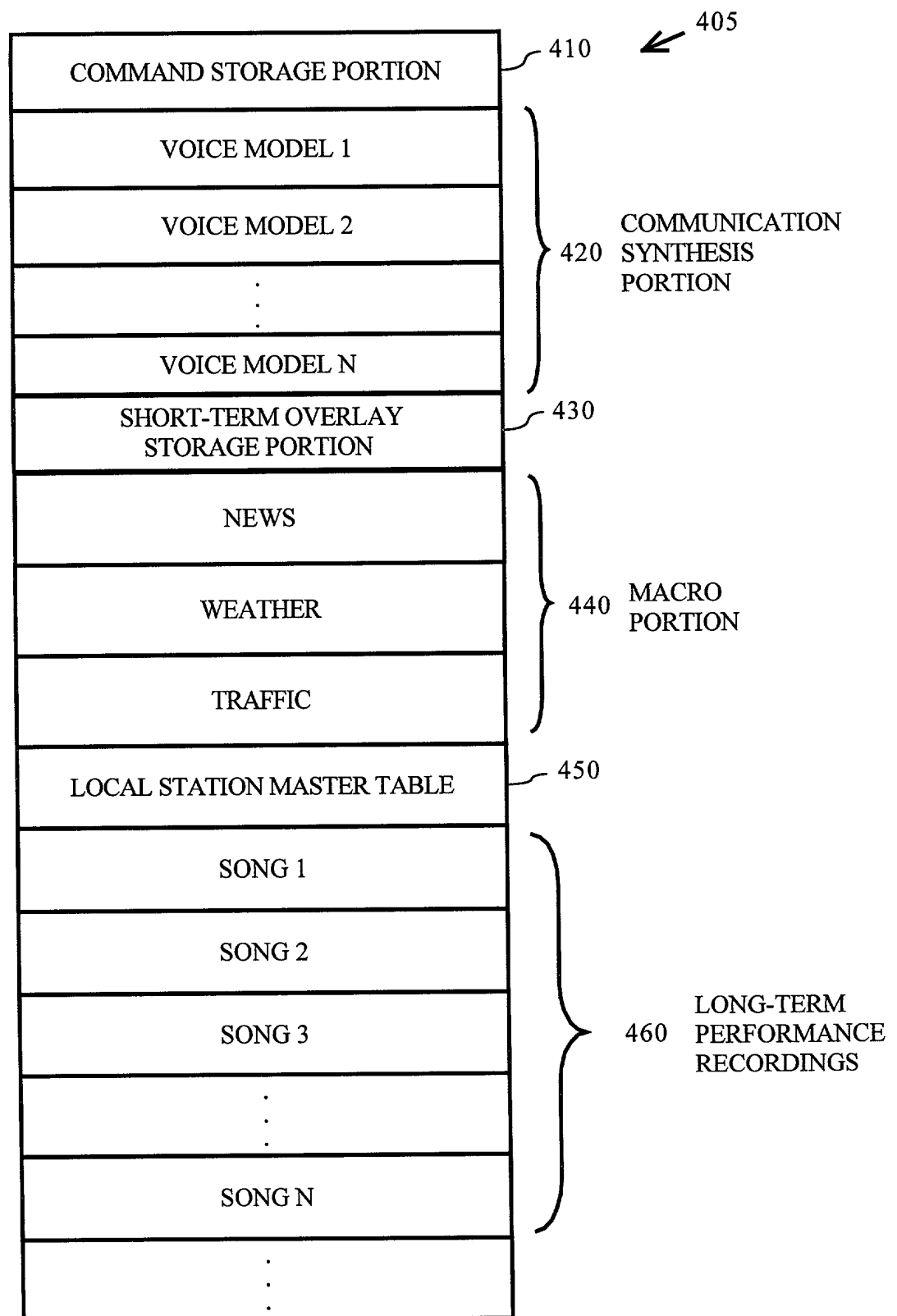


FIG. 6

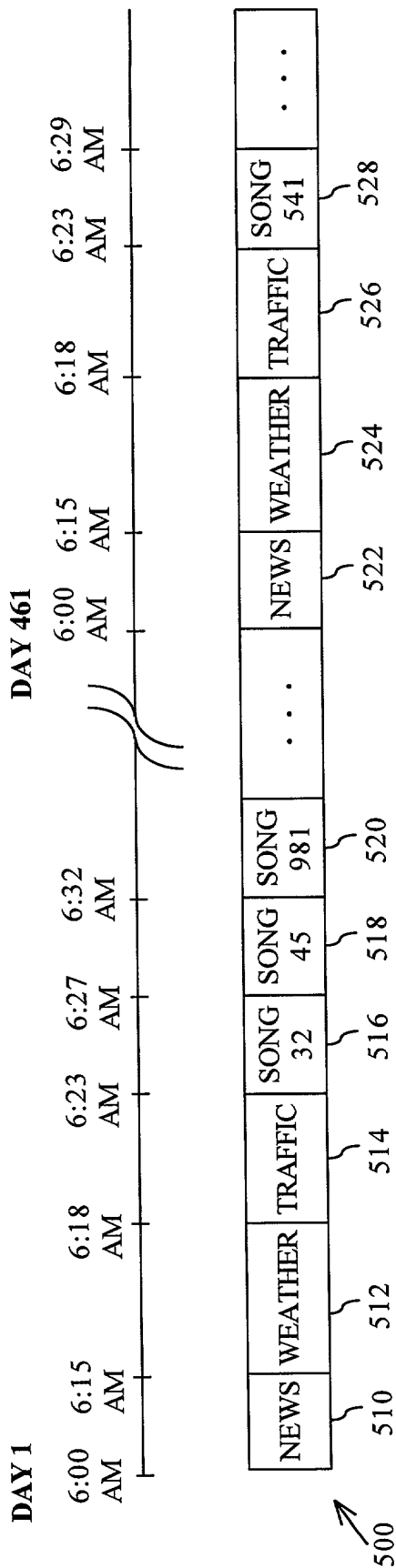


FIG. 7

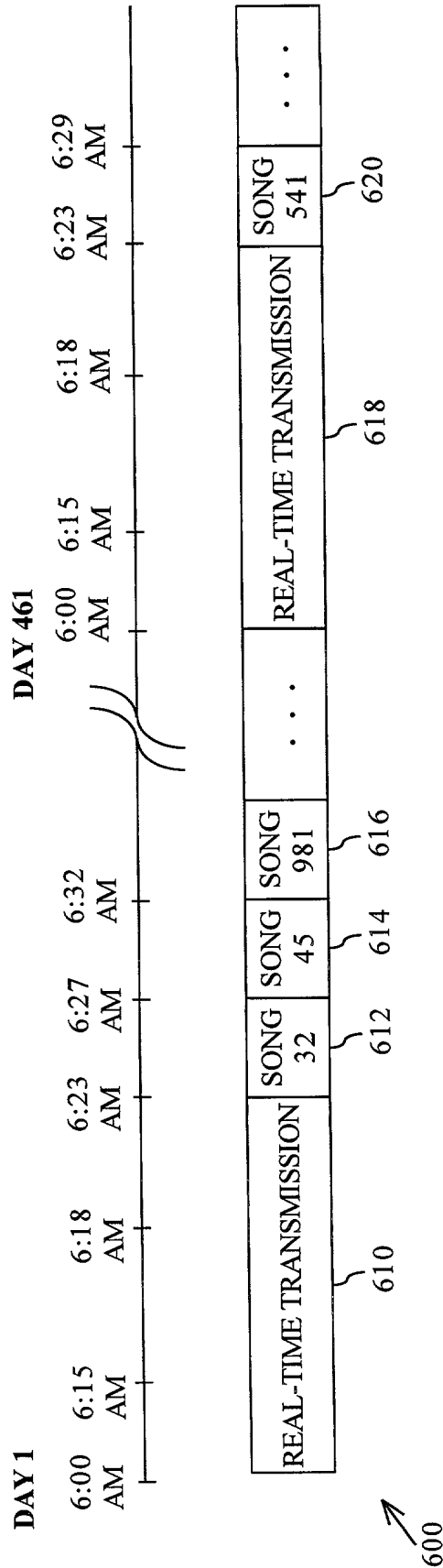


FIG. 8

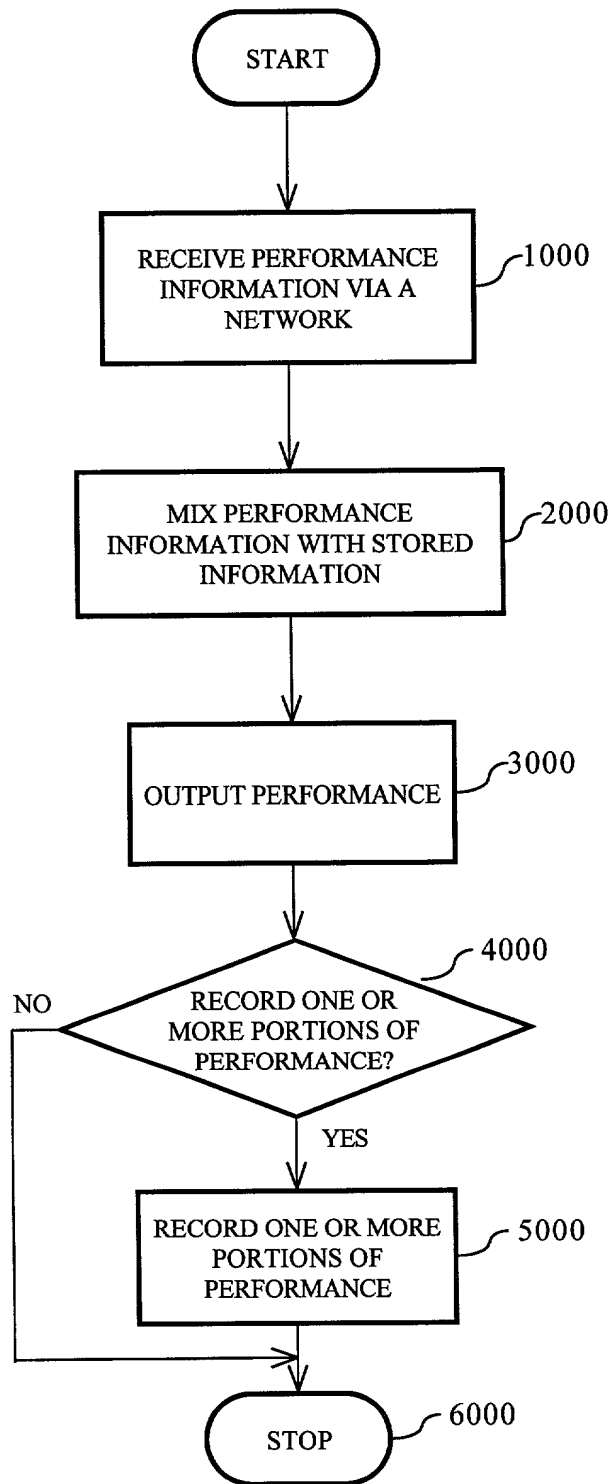


FIG. 10

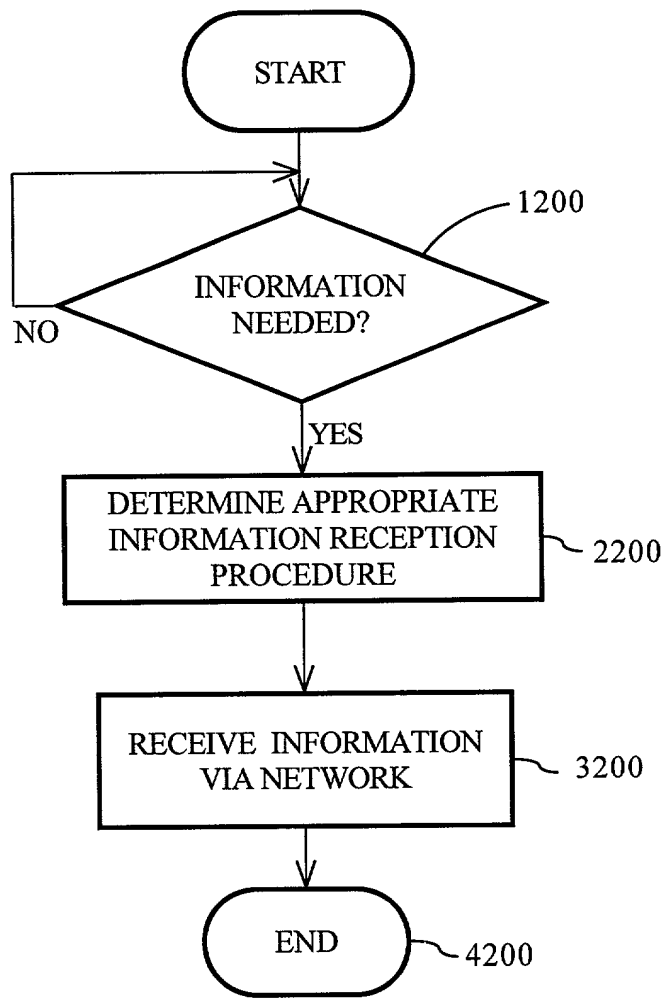


FIG. 11

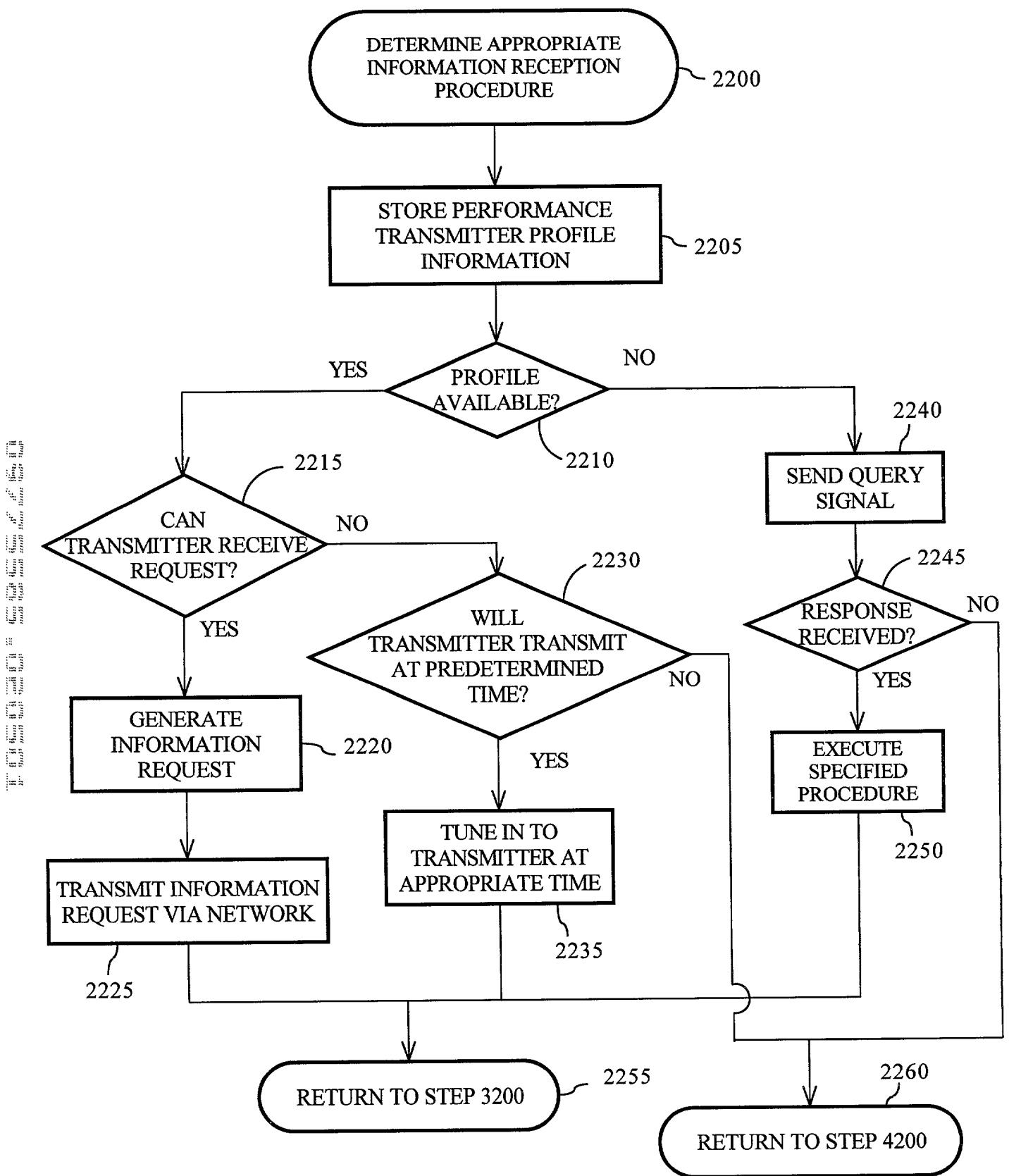


FIG. 12

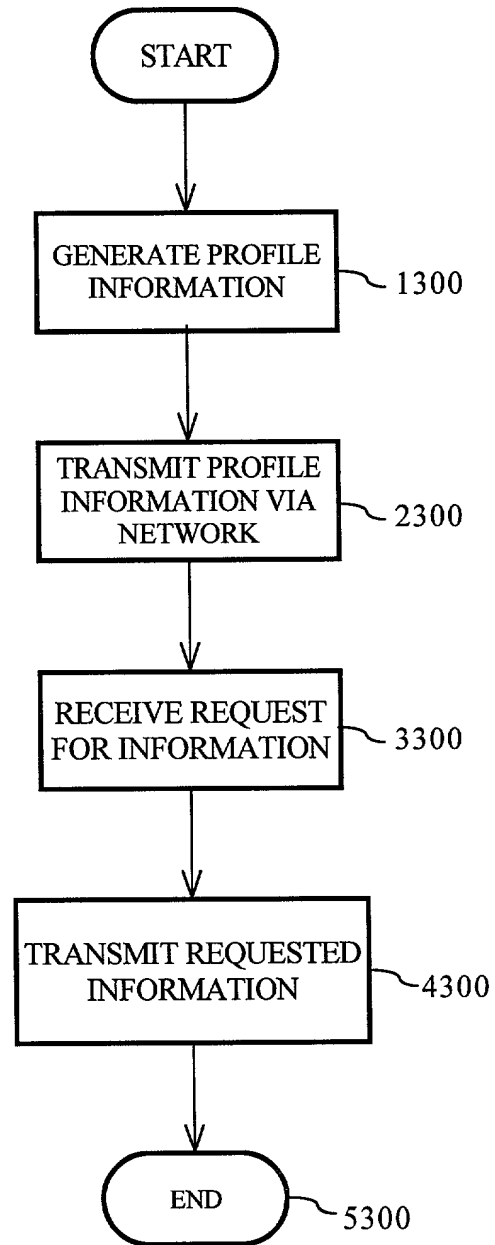


FIG. 13